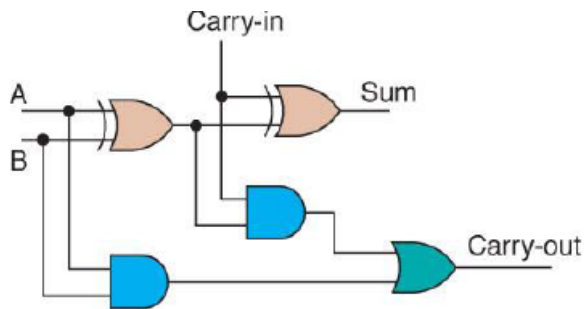


慈濟大學 111 學年度 碩博士班、博士學位學程暨碩士在職專班 招生考試命題紙

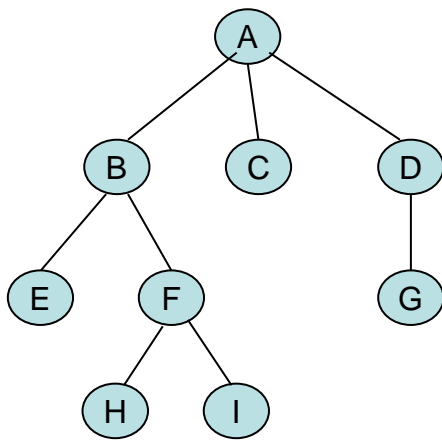
科目：計算機概論

共1頁

1. (20%) 用真值表說明下列全加器(full adder)電路的行為，並寫出 Sum 與 Carry-out 的邏輯表示式。



2. (20%) 請說明網路技術中 Packet switching 與 Circuit Switching 的交換方法，並比較兩者的優缺點。
3. (30%) (a) Represent the following tree by a binary tree representation.



- (b) Assume the above binary tree is represented by a one-dimensional array (without pointer, and the array index is started from 0). Show the array representation and give the array index for nodes C, E, G and H.
4. (30%) Explain the following terms:
- (a) machine cycle in CPU.
 - (b) paged memory management.
 - (c) Round-robin approach in CPU process scheduling.